

Customer No.: 31561
Application No.: 10/604,821
Docket No.: 11208-US-PA

REMARKS

Present Status of the Application

Claims 1-11 are pending of which claims 1, 2, 4, 5, 6 and 7 have been amended and claim 3 has been cancelled without prejudice or disclaimer in order to more explicitly describe the claimed invention. Further, Figures 6 and 7 have been amended to be designated a legend, "PRIOR ART." in a separate accompanying page. Applicants respectfully submit that upon acceptance of the proposed amendments to Figures 6 and 7 by the Examiner and allowance of this application, a formal corrected drawing will be submitted. It is believed that no new matter adds by way of amendments made to claims or otherwise to the application. For at least the foregoing reason, Applicants respectfully submit that claims 1-2 and 4-11 patentably define over prior art of record and reconsideration of this application is respectfully requested.

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Discussion of objection to Abstract

1. *The Office Action objected to the abstract under MPEP 608.01(b) for failing to describe any key inventive features of the power detector and the power detecting method*

In response thereto, Applicants would like to thank the Examiner for pointing out the informalities in the abstract and accordingly Applicant amended the abstract. The amended abstract discloses key inventive features of the power detector and the power detecting method. Reconsideration is respectfully requested.

Discussion of objection to claims 2 and 5

2. *The Office Action objected to claim 2 because of informalities.*
3. *The Office Action objected to claim 5 because of informalities.*

In response thereto, Applicants would like to thank the Examiner for pointing out the informalities and accordingly Applicants amended Claims 2 and 5. In line 7 in claim 2, the phrase of "the second power source" is amended to "a second power source." Also, in line 2 in claim 5, the phrase of "a first output signal" is amended to "the first output signal." Reconsideration is respectfully requested.

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Discussion of objection to drawings

4. *The Office Action objected to figures 6 and 7 in the drawings under 37 CFR 1.21(d).*

Figures 6 and 7 in the drawings failed to be designed a legend such as – Prior Art.

In response thereto, Applicants would like to thank the Examiner for pointing out the informalities and accordingly Applicant amended Figures 6 and 7 so that they were designed a legend, "PRIOT ART." Reconsideration is respectfully requested.

Discussion of the claims rejection under 35 USC 102(b)

6. *The Office Action rejected Claims 1-5 under 35 U.S.C. 102(b) as being anticipated by Nagasawa (U.S. Pat. No. 5,424,994 hereinafter Nagasawa)*

Applicants respectfully disagree and traverse the above rejections as follows.

From the column 3 lines 26-34, Nagasawa discloses "The power supply means is used for switching a power source by which electric power is supplied to the volatile memory means from the first power source to the second power source when the first power source is cut off, and for switching it from the second power source to the battery when a service interruption of the second power source occurs." Therefore, invention motives and functionalities of Nagasawa are to devise a power source switching circuit for switching a plurality of external power sources according to different cases. In contrast, from page 1, lines 16-19 and step 110 in Fig.1, a first external power source(a DRAM operating power source) and a second external power source (a DRAM data power source) of the present application must be used concurrently, when a Dynamic Random

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Access Memory (DRAM) operates. Therefore, independent claims 1 and 2 are amended so that the present application is confined to be exploited in a DRAM, as showed in amended claims 1 and 2 in the "amendments to claims." Furthermore, Nagasawa doesn't teach, suggest or disclose "After the IC receives the data power level (referred to a second power source as claimed in claim 1) indication from the second power source detector, the IC automatically configures itself to operate at the corresponding data power level" as disclosed in page 3, lines 22-25 in the present application.

In addition, from page 8 lines 11-19, the present application discloses a method for determining what the current data power level is so that a DRAM do not need to present to a fixed data level during the IC fabrication, and the data power level of the DRAM data power lines is automatically self-configured during the DRAM power-up. Therefore, Nagasawa and the application have their different invention motives and functionalities. That is, Nagasawa fails to teach, suggest or disclose "A method of detecting power sources in a Dynamic Random Access Memory, which comprises: receiving a first power source, and a second power source;....." as claimed in the amended claim1. Also, Nagasawa fails to teach, suggest or disclose "A method of detecting power sources in a Dynamic Random Access Memory, which comprises: a first power source detector for; a second power source detector for...." as claimed in the amended claim 2. Hence, independent claim 2 is patentable over Nagasawa and claims 4 and 5, which depend on their base amended claim 2, are not anticipated by

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Nagasawa and allowable as a matter of law, for at least the reason that dependent claims 4 and 5 contain all features of their base amended claim 2.

For at least the above reasons, Applicants respectfully submit that claims 1, 2, 4 and 5 patently define over Nagasawa and should be allowed. Reconsideration and withdrawal of these rejections is respectfully requested.

Discussion of the claims rejection under 35 USC 103(a)

8. The Office Action rejected Claims 7-10 under 35 U.S.C. 103(a) as being anticipated by Nagasawa (U.S. Pat. No. 5,424,994 hereinafter Nagasawa)

Applicants respectfully disagree and traverse the above rejections as follows. The examiner's rejection claims 7-10 is base on an assumption that Nagasawa discloses a circuit as disclosed in claim 2. However, as discussed above, since claim 2 is amended to the amended claim 2, Nagasawa fails to teach, suggest or disclose "A method of detecting power sources in a Dynamic Random Access Memory, which comprises: a first power source detector for; a second power source detector for...." as claimed in the amended claim 2. Therefore claim 2 is not disclosed by Nagasawa. So, dependent claims 7-10, which directly or indirectly depend on their base claim 2, are not anticipated by Nagasawa and allowable as a matter of law, for at least the reason that because dependent claims 7-10 contain all features of their base amended claim 2.

For at least the above reasons, Applicants respectfully submit that claims 7-10

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patently define over Nagasawa and should be allowed. Reconsideration and withdrawal of these rejections is respectfully requested.

CONCLUSION

For at least the foregoing reasons, it is believed that all the pending claims 1, 2, 4-11 of the present application patently define over the prior art and are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Respectfully submitted,

Date :

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